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PATENT APPLICATION

ATTORNEY DOCKET NO. 10030577-1

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Laura Marie Bundy, et al.

Serial No.:

Examiner:

Filing Date: 04-19-2004

Group Art Unit:

Title: APPARATUS, SYSTEM AND/OR METHOD FOR COMBINING MULTIPLE TESTS TO A SINGLE TEST IN A MULTIPLE INDEPENDENT PORT TEST ENVIRONMENT

COMMISSIONER FOR PATENTS

PO Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97(b), or  
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97(c) together with either a:  
☐ Statement under 37 CFR 1.97(e), or  
☐ a \$180.00 Processing fee under 37 CFR 1.17(p), or  
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97 (d) together with a:  
☐ Statement under 37 CFR 1.97(e), and  
☐ a \$180.00 processing fee under 37 CFR 1.17(p).  
(Filed after final action or notice of allowance, whichever occurs first, but before payment of the issue fee)

Please charge to Deposit Account **50-1078** the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **50-1078** pursuant to 37 CFR 1.25.

☒ Applicant(s) submit herewith Form PTO 1449. References identified with an asterisk (\*) were disclosed in Patent Application No. \_\_\_\_\_ filed \_\_\_\_\_, now U. S. Patent No. \_\_\_\_\_, and, as such, copies thereof are not included pursuant to the provisions of 37 CFR 1.98(d).

☐ A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individuals(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

"Express Mail" label no. **EL 964692913 US**

Date of Deposit **04-19-2004**

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450.

By \_\_\_\_\_

Typed Name: **Gregory W. Osterloth**

Respectfully submitted,

**Laura Marie Bundy, et al.**

By \_\_\_\_\_

**Gregory W. Osterloth**

Attorney/Agent for Applicant(s)  
Reg. No. **36,232**

Date: **04-19-2004**

<b>FORM PTO-1449</b>  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>  (Use several sheets if necessary)	ATTY. DOCKET NO.	SERIAL NO.
	10030577-1	
	APPLICANT <b>Laura Marie Bundy, et al.</b>	
	FILING DATE	GROUP
	04-19-2004	

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	*	DOCUMENT NUMBER	DATE	NAME

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	TRANSLATION	
					YES	NO

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	Agilent Technologies, www.agilent.com/see/concurrenttest, "Agilent Concurrent Test" November 1, 2001, 2 pages.
	Agilent Technologies, www.agilent.com/cgi-bin/bvpub/agilent/editorial, "Concurrent Test Backgrounder", August 13, 2003, 3 pages.
	go/semiconductor, "Concurrent Test allows parallel testing of IPblocks within each device", Autumn 2001, page 16, 1 page.

EXAMINER	DATE CONSIDERED
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\* Copies of these references are not enclosed pursuant to 37 CFR 1.98(d). (See accompanying IDS)

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		DOCUMENT NUMBER	DATE	NAME	TRANSLATION	
					YES	NO

**OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)**

	"Mark LaPedus, Silicon Strategies, <a href="http://www.siliconstrategies.com">www.siliconstrategies.com</a> , "Agilent claims breakthrough in 'test reuse' for SoCs", May 30, 2003, 2 pages.
	Jeff Chappell, Electronic News, <a href="http://www.reed-electronics.com">www.reed-electronics.com</a> , "Agilent Unveils Core Test Language Browser", May 30, 2003, 2 pages.

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